

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,068,197 B1
APPLICATION NO. : 10/821517
DATED : June 27, 2006
INVENTOR(S) : Tsyrganovich

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 12, line 67, the following text should be added after the words "in a second mode" and before the semicolon --, wherein the sigma-delta converter portion includes a sigma-delta modulator and a filter--

Thus, lines 63-68 of column 12 should read:

1. A circuit comprising:
a sigma-delta converter portion that outputs a digital data stream with a digital amplitude, the digital data stream being N bits wide in a first mode and M bits wide in a second mode, wherein the sigma-delta converter portion includes a sigma-delta modulator and a filter; and

Signed and Sealed this

Twenty-ninth Day of August, 2006

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looped initial "J" and a distinct "D" at the end.

JON W. DUDAS
Director of the United States Patent and Trademark Office